

**Notice of Allowability**

Application No.

10/769,381

Examiner

ANH PHUNG

Applicant(s)

MORIKAWA, YOSHINAO

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-- The MAILING DATE of this communication appears on the cover sheet with the correspondence address--

All claims being allowable, PROSECUTION ON THE MERITS IS (OR REMAINS) CLOSED in this application. If not included herewith (or previously mailed), a Notice of Allowance (PTOL-85) or other appropriate communication will be mailed in due course. **THIS NOTICE OF ALLOWABILITY IS NOT A GRANT OF PATENT RIGHTS.** This application is subject to withdrawal from issue at the initiative of the Office or upon petition by the applicant. See 37 CFR 1.313 and MPEP 1308.

1. ☐ This communication is responsive to \_\_\_\_.
2. ☒ The allowed claim(s) is/are 1-21.
3. ☒ The drawings filed on 30 January 2004 are accepted by the Examiner.
4. ☒ Acknowledgment is made of a claim for foreign priority under 35 U.S.C. § 119(a)-(d) or (f).
- a) ☒ All b) ☐ Some\* c) ☐ None of the:
1. ☒ Certified copies of the priority documents have been received.
2. ☐ Certified copies of the priority documents have been received in Application No. \_\_\_\_.
3. ☐ Copies of the certified copies of the priority documents have been received in this national stage application from the International Bureau (PCT Rule 17.2(a)).
- \* Certified copies not received: \_\_\_\_.

Applicant has THREE MONTHS FROM THE "MAILING DATE" of this communication to file a reply complying with the requirements noted below. Failure to timely comply will result in ABANDONMENT of this application.  
**THIS THREE-MONTH PERIOD IS NOT EXTENDABLE.**

5. ☐ A SUBSTITUTE OATH OR DECLARATION must be submitted. Note the attached EXAMINER'S AMENDMENT or NOTICE OF INFORMAL PATENT APPLICATION (PTO-152) which gives reason(s) why the oath or declaration is deficient.
6. ☐ CORRECTED DRAWINGS (as "replacement sheets") must be submitted.
- (a) ☐ including changes required by the Notice of Draftsperson's Patent Drawing Review (PTO-948) attached
- 1) ☐ hereto or 2) ☐ to Paper No./Mail Date \_\_\_\_.
- (b) ☐ including changes required by the attached Examiner's Amendment / Comment or in the Office action of Paper No./Mail Date \_\_\_\_.
- Identifying indicia such as the application number (see 37 CFR 1.84(c)) should be written on the drawings in the front (not the back) of each sheet. Replacement sheet(s) should be labeled as such in the header according to 37 CFR 1.121(d).
7. ☐ DEPOSIT OF and/or INFORMATION about the deposit of BIOLOGICAL MATERIAL must be submitted. Note the attached Examiner's comment regarding REQUIREMENT FOR THE DEPOSIT OF BIOLOGICAL MATERIAL.

**Attachment(s)**

1. ☒ Notice of References Cited (PTO-892)
2. ☐ Notice of Draftsperson's Patent Drawing Review (PTO-948)
3. ☒ Information Disclosure Statements (PTO-1449 or PTO/SB/08), Paper No./Mail Date 1/30/04
4. ☐ Examiner's Comment Regarding Requirement for Deposit of Biological Material
5. ☐ Notice of Informal Patent Application (PTO-152)
6. ☐ Interview Summary (PTO-413), Paper No./Mail Date \_\_\_\_.
7. ☐ Examiner's Amendment/Comment
8. ☒ Examiner's Statement of Reasons for Allowance
9. ☒ Other Search History.



**ANH PHUNG**  
**PRIMARY EXAMINER**

### **DETAILED ACTION**

1. In response to the U.S. Pat. Application Ser. No. 10/769,381 filed on January 30, 2004, claims **1-21** are pending in the application.

#### ***Priority***

2. Receipt is acknowledged of papers submitted under 35 U.S.C. 119(a)-(d), which papers have been placed of record in the file.

#### ***Information Disclosure Statement***

3. This office acknowledges receipt of the following items from the Applicant:  
Information Disclosure Statement (IDS) filed on January 30, 2004.  
Information disclosed and listed on PTO 1449 was considered.

#### ***Examiner's Statement of Reasons for Allowance***

4. Claims **1-21** are allowed.

Ishizuka (U.S. Pat. No. 6,473,327), Choi (U.S. Pat. No. 6,226,214), Jyouno et al. (U.S. Pat. No. 5,892,713) and Suminaga et al. (U.S. Pat. No. 5,757,709) disclose a read only memory device similar to that of the present application, but fail to teach:

"wherein said memory cell array is constituted by connecting the first electrodes of said memory cells in the same row to a common word line, connecting said second electrodes each other between two of said memory cells adjacent to each other in the row direction, connecting one-hand said second electrodes in said memory cells in the same column to a common bit line, and connecting the other-hand said second electrodes in said memory cells in the same column to a common virtual ground line and moreover constituted by being divided into at least a plurality columns of subarrays,

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memory cell columns at the both ends of said subarrays are constituted so that said second electrodes are not connected each other between two of said memory cells adjacent to each other in the row direction at the both sides of boundaries between said subarrays but they are separated from each other and respectively connected to an independent bit line or virtual ground line, and one of said word lines, one of said bit lines, and one of said virtual ground lines are selected for each of said subarrays and one memory cell from which data will be read is selected” as claimed in the independent claim 1; or

“wherein said memory cell array is constituted by connecting the first electrodes of said memory cells in the same row to a common word line, connecting said second electrodes each other between two of said memory cells adjacent in the row-direction, connecting one-hand said second electrodes in said memory cells in the same column to a common bit line, and connecting the other-hand said second electrodes in said memory cells to a common virtual ground line and moreover constituted by being divided into at least a plurality columns of subarrays, in the case of memory cell columns at the both ends of said subarrays, said second electrodes are not connected each other between two of said memory cells adjacent in the row direction at the both sides of boundaries between said subarrays but they are separated from each other and connected to independent bit lines or virtual ground lines, and each of said bit lines and each of said virtual ground lines respectively have functions of the both and when one of them executes the function of the other, the other executes the function of the former” as claimed in the independent claim 2.

Any comments considered necessary by applicants must be submitted no later than the payment of the issue fee and, to avoid processing delays, should preferably accompany the issue fee. Such submissions should be clearly labeled "Comment on Statement of Reasons for Allowance".

***Conclusion***


5. Any inquiry concerning this communication or earlier communications from the examiner should be directed to **ANH PHUNG** whose telephone number is **(571) 272-1883**. The examiner can normally be reached on Monday-Friday from 8:00 AM to 4:30 PM.

If attempts to reach the examiner by telephone are unsuccessful, the examiner's supervisor, **RICHARD ELMS**, can be reached on **(571) 272-1869**. The fax phone number for the organization where this application or proceeding is assigned is **(703) 872-9306**.

Information regarding the status of an application may be obtained from the Patent Application Information Retrieval (PAIR) system. Status information for published applications may be obtained from either Private PAIR or Public PAIR. Status information for unpublished applications is available through Private PAIR only. For more information about the PAIR system, see <http://pair-direct.uspto.gov>. Should you have questions on access to the Private PAIR system, contact the Electronic Business Center (EBC) at **866-217-9197** (toll-free).

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AKP

  
**ANH PHUNG  
PRIMARY EXAMINER**